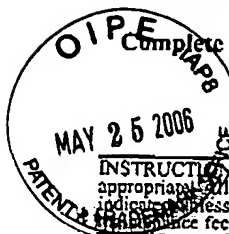


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 May 25, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/809,044	03/25/2004	Shiro Fujima	NEKU 21.077 (100806-00256)	5789

TITLE OF INVENTION: REDUNDANCY CONTROL CIRCUIT WHICH SURELY PROGRAMS PROGRAM ELEMENTS AND SEMICONDUCTOR MEMORY USING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/07/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
MAI, SON LUU	2827	365-200000

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Elpida Memory, Inc.

Tokyo, Japan

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Date May 24, 2006

Typed or printed name

Brian B. HennesseyRegistration No. 51,271

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Date	Client/Matter Number
May 25, 2006	100806-00256
From	Attorney Number
James J. Dade	03290
Phone	Fax
212.940.6731	(212) 940-8986

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U.S. Patent Application No.: 10/809,044

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